

BALOGH

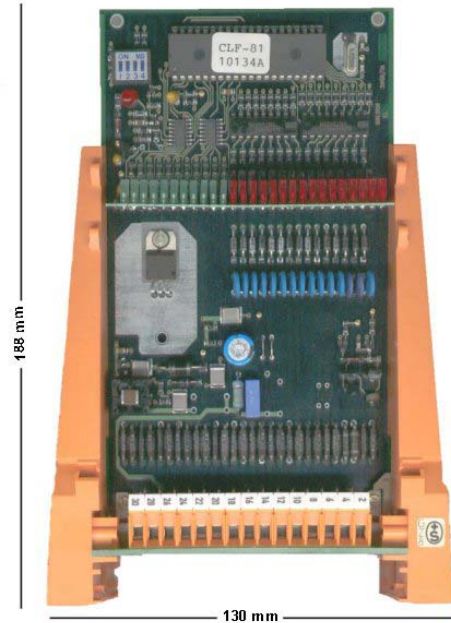


CLF-81 Control Board

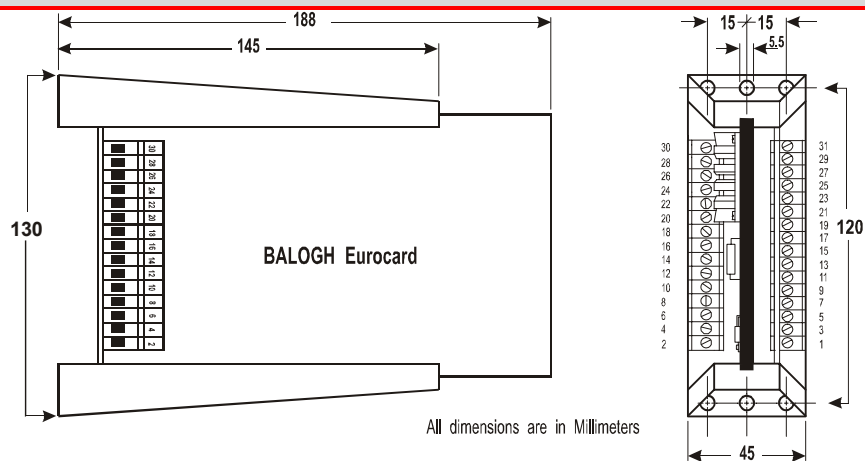
Identification - Coding

Characteristics

- Parallel Control Card, Eurocard Format (100 x 160mm)
- Multiplexable parallel connection
- Allows Reading of type "OF/OFR" Read Only TAGS.
- Reads and Buffers entire "OF/OFR" TAG (7 bytes)
- Echoes each byte Read according to the selected address
- Each Control Board must be connected to a BALOGH Transceiver in order to Read data from the TAGS.
- Requires GC-01 Board Holder



Dimensions



Revised June 27, 2002

BALOGH 7699 Kensington Court - Brighton, MI 48116-8561 - (248) 486-RFID - Subject to Modifications

Characteristics at 25° C	Symbol	Unit	CLF-81
V Supply (< 2% Ripple)	Vcc	V DC	24
Voltage Tolerance			-10% to +10%
Current Consumption	Im	mA	150
Serial Connection			No
No. of Parallel Inputs			5
Input Impedance	Ze	K ohm	10
Input Logic 0		V	0 to 10
Input Logic 1		V	15 to Vcc
No. of Parallel Outputs			13
MAX Continuous Current (per Output)	Is	mA	100
MAX Voltage Drop across an Output	Vdrop	V	1.5
Output Logic 0		V	0
Output Logic 1		V	Vcc – 1.5
MIN Ambient TEMP	Tmin	°C	0
MAX Ambient TEMP	Tmax	°C	+70
Protection Degree	IP		00
Weight	M	g	300
MAX Cable Length Between Control Board and Transceiver			1000 ft
MAX Cable Length Between Control Board and Buffer			
Short Circuit Protected			
Protected against Inverse Polarity			Yes

Terminal	Locations	ER*71/85	ER*80
1	Transceiver Output Connected to	E	Term 3
2	Bit 2 Output - MSB		
3	Bit 1 Output – Echo of the 3 bit		
4	Bit 0 Output Address LSB		
5	Bit 7 Output (MSB)		
6	Bit 6 Output		
7	Bit 5 Output		
8	Bit 4 Output		
9	Bit 3 Output		
10	Bit 2 Output		
11	Bit 1 Output		
12	Bit 0 Output (LSB)		
13	VAL Output		
14	PRE Output		
15			
16			
17			
18			
19			
20			
21			
22	Bit 2 Input		
23	Bit 1 Input – Address Selection		
24	Bit 0 Input 0 - 6		
25	LEC Input		
26	SBA Input (Multiplex Line)		
27	Transceiver Input Connected to	S	Term 2
28			
29			
30	+24 VDC to Board & Transceiver	V	Term 1
31	Ground	O	Term 4